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U.S. Patent Application Serial No. 10/646,922
Response to Final OA dated April 9, 2008

REMARKS

Claim 2 has been amended in order to more particularly point out, and distinctly claim the subject matter which the Applicants regard as their invention. The Applicants respectfully submit that no new matter has been added. Claims 1 and 3-7 are cancelled without prejudice or disclaimer. It is believed that this Amendment is fully responsive to the Office Action dated April 9, 2008.

In the Office Action, Claims 1-7 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Kweon et al. (U.S. Patent No. 6,925,313) in view of the cited prior art document JP 59-78654. Reconsideration and removal of this rejection are respectfully requested in view of the cancelling of Claims 1 and 3-7, the amendments to Claim 2 and the following remarks.

The Office Action comments on Applicants' previous arguments, as follows:

The Examiner agrees that JP 59-78654 does not show two circuit boards, but it is noted that the rejection doesn't say JP 59-78654 teaches two circuit boards. The examiner points to two locations in Fig. 3 that is viewed as anticipating the Applicants' claim limitations of how a group electronic circuit chips are mounted. It is also noted, as implied in the St. Regis Paper Co. v. Bemis Co., Inc. decision (193 USPQ 8, 11 (7th Cir. 1977), duplicating parts for a multiplied effect (i.e. implementing an electronic chip mounting location design philosophy twice) is not necessarily a patentable advance if the philosophy is known/obvious.

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The Office Action maintains the rejection because within the housing of a foldable electronic device, there is a limited amount of space and a finite number of operational parts to be functionally placed and it is well within the scope of one of ordinary skill in the art to find the combination of mounting locations (possibly through trial and error) that allows for the dissipation of heat (LCD driver circuit chips [i.e., amplifiers] should not be mounted directly above/below each other because it can stifle heat dissipation) in order to make the device as thin as possible to be aesthetically pleasing and convenient for the user to carry.

In the foldable electronic device of Claim 2, as amended, a group of electronic circuit chips in the chip mount area of the flexible lead extending from the subdisplay and a group of the electronic circuit chips in the chip mount area of the flexible lead extending from the main display are positioned in a staggered meshing relation within the opening of the frame.

In JP '564, two chip mount areas opposed to each other have a plurality of electronic circuit chips, respectively. The electronic circuit chips on one chip mount area and the electronic circuit chips on the other chip mount area are positioned in a meshing relation. However, JP '564 fails to disclose a structure in which a plurality of electronic chips on the two chip mount areas is in a meshing relation within an opening formed in a frame.

In Kweon, the chips of the LCD (27) and the chips on the LCD (28) are in a staggered relation, however, the chips on the LCD (27) and the chips on the LCD (28) are not in a staggered meshing relation within an opening formed in a frame. Therefore, it is respectfully submitted that the structure of the present invention cannot be achieved by combining Kweon and JP '564. In view

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of the amendment to Claim 2, and the above remarks, removal of this rejection is respectfully requested.

In view of the aforementioned amendments and accompanying remarks, Claim 2, as amended, is believed to be patentable and in condition for allowance, which action, at an early date, is requested.

If, for any reason, it is felt that this application is not now in condition for allowance, the Examiner is requested to contact the Applicants' undersigned attorney at the telephone number indicated below to arrange for an interview to expedite the disposition of this case.

In the event that this paper is not timely filed, the Applicants respectfully petition for an appropriate extension of time. Please charge any fees for such an extension of time and any other fees which may be due with respect to this paper, to Deposit Account No. 01-2340.

Respectfully submitted,

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